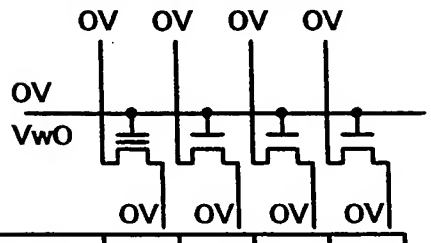
FIG. 1A

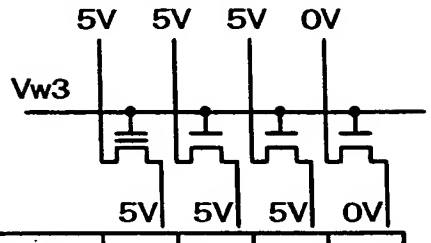
INITIAL STATE



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	00
DISTUR-BANCE	–	–	–	–

FIG. 1B

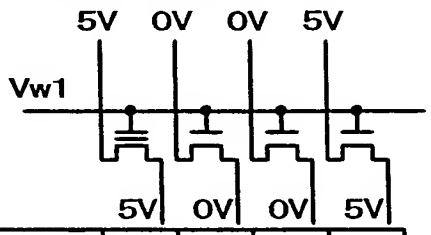
WRITE #1



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	11
DISTUR-BANCE	++	–	–	–

FIG. 1C

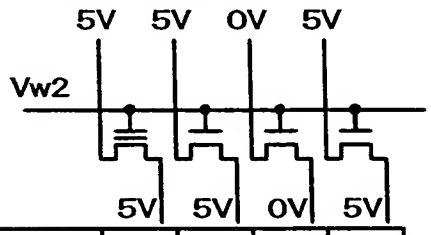
WRITE #2



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	01	11
DISTUR-BANCE	++	–	–	+

FIG. 1D

WRITE #3



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	10	11
DISTUR-BANCE	++	++	–	+

FIG. 1E

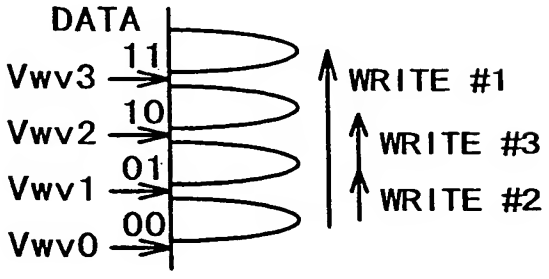
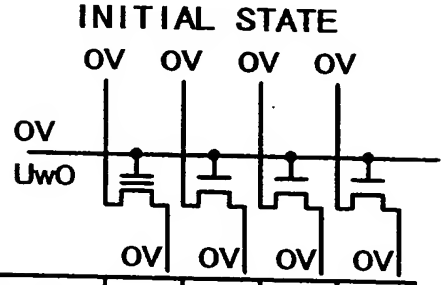
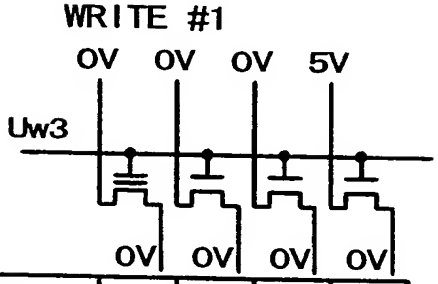


FIG. 2A



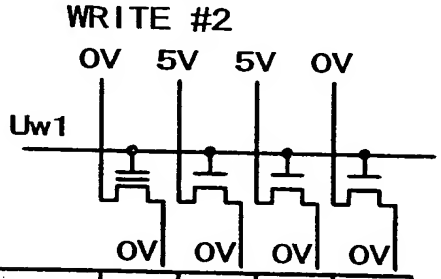
WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	00
DISTUR-BANCE	–	–	–	–

FIG. 2B



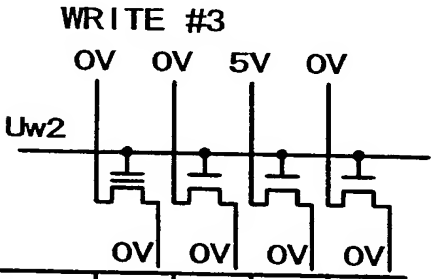
WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	11
DISTUR-BANCE	++	–	–	–

FIG. 2C



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	01	11
DISTUR-BANCE	++	–	–	+

FIG. 2D



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	10	11
DISTUR-BANCE	++	+	–	+

FIG. 2E

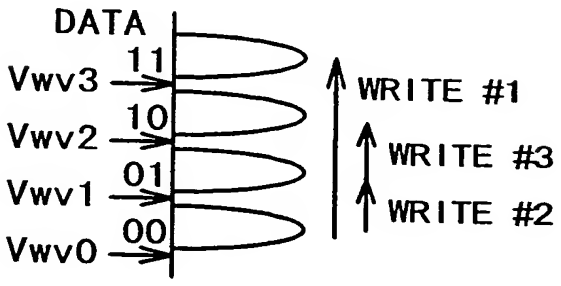
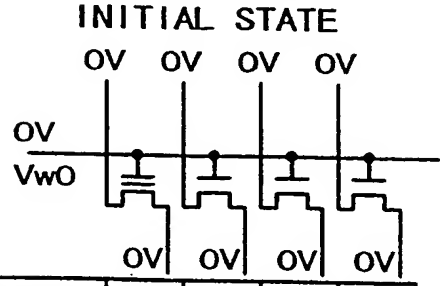
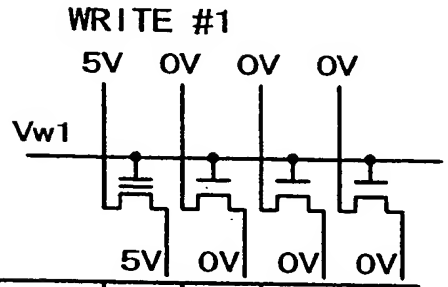


FIG. 3A (PRIOR ART)



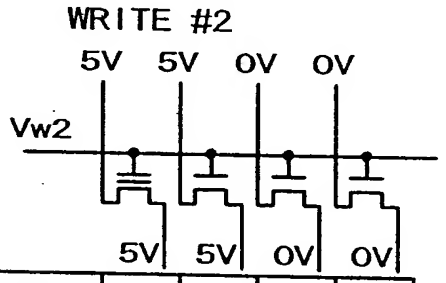
WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	00
DISTUR-BANCE	–	–	–	–

FIG. 3B (PRIOR ART)



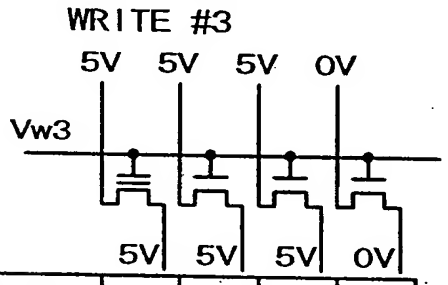
WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	01	11
DISTUR-BANCE	++	–	–	–

FIG. 3C (PRIOR ART)



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	10	10
DISTUR-BANCE	++	++	–	–

FIG. 3D (PRIOR ART)



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	10	11
DISTUR-BANCE	++	++	++	–

FIG. 3E (PRIOR ART)

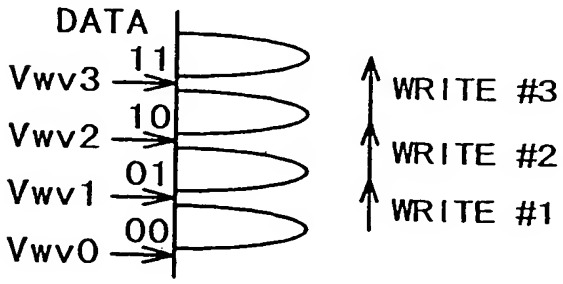
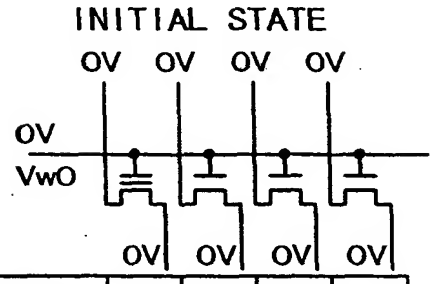
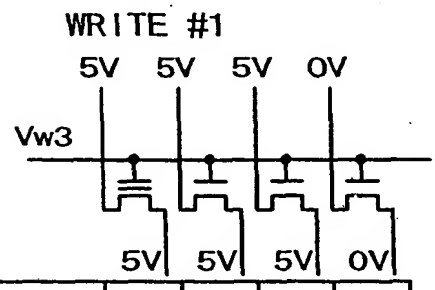


FIG. 4A (PRIOR ART)



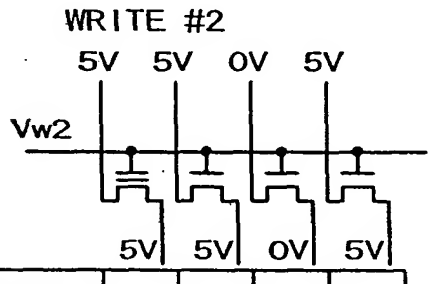
WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	00
DISTUR-BANCE	–	–	–	–

FIG. 4B (PRIOR ART)



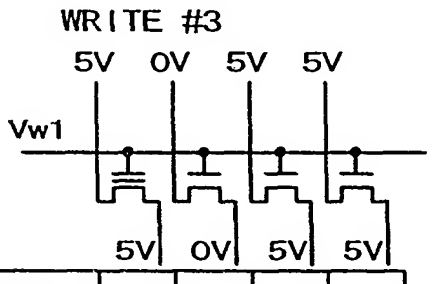
WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	00	11
DISTUR-BANCE	++	–	–	–

FIG. 4C (PRIOR ART)



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	00	10	11
DISTUR-BANCE	++	–	–	+

FIG. 4D (PRIOR ART)



WRITE TARGET DATA	00	01	10	11
MEMORY CELL DATA	00	01	10	11
DISTUR-BANCE	++	–	+	+

FIG. 4E (PRIOR ART)

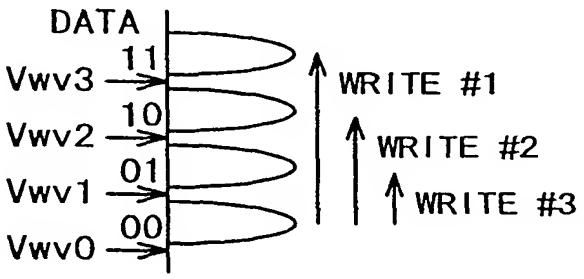


FIG. 5A (PRIOR ART)

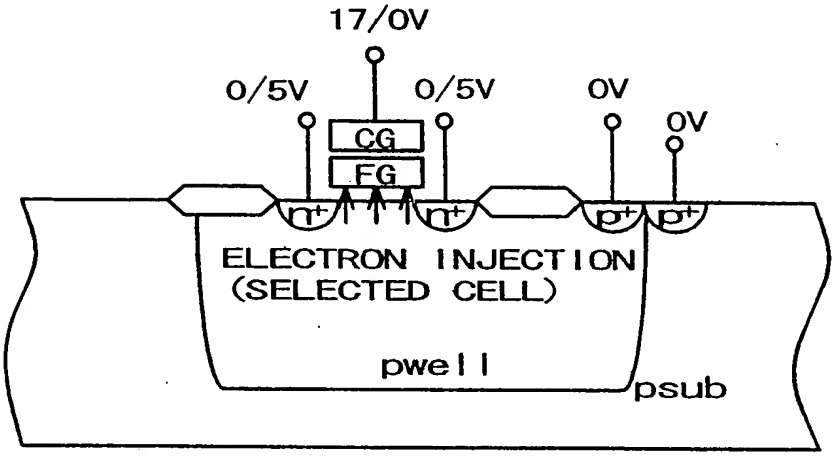


FIG. 5B (PRIOR ART)

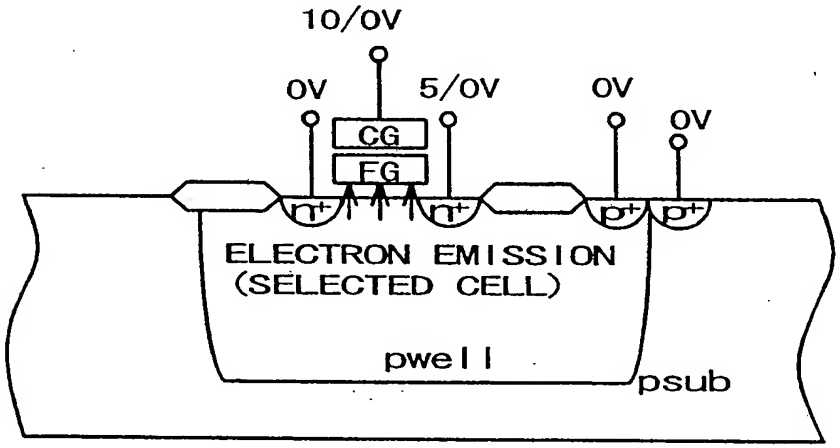


FIG. 6 (PRIOR ART)

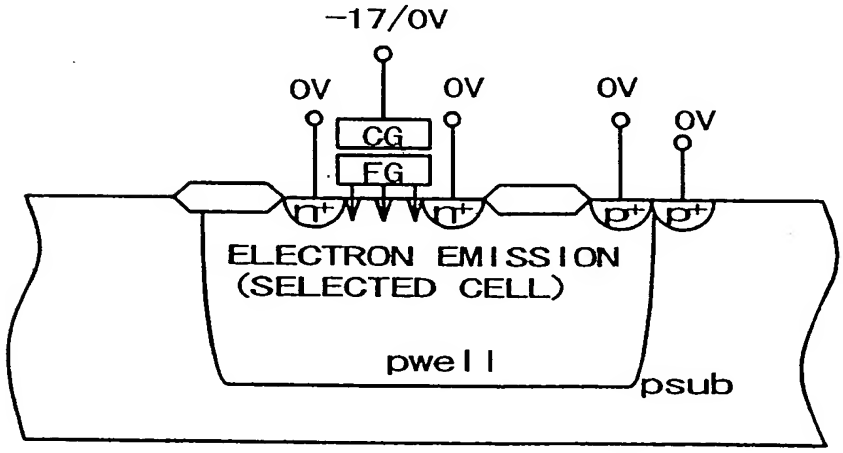


FIG. 7

	PRESENT INVENTION	PRIOR ART 1	PRIOR ART 2
WRITE #1	1	13	1
WRITE #2	13	21	27
WRITE #3	21	1	13
PULSES	35	35	41

FIG. 8

	PRESENT INVENTION	PRIOR ART 1	PRIOR ART 2
WRITE TIME PERIOD	1449 μSECONDS	1449 μSECONDS	1695 μSECONDS
"00" LEVEL DISTURBANCE	0.381V	0.382V	0.382V
"01" LEVEL DISTURBANCE	0.001V	0.097V	0.000V
"10" LEVEL DISTURBANCE	0.000V	0.017V	0.000V
"11" LEVEL DISTURBANCE	0.000V	0.000V	0.000V

FIG. 9

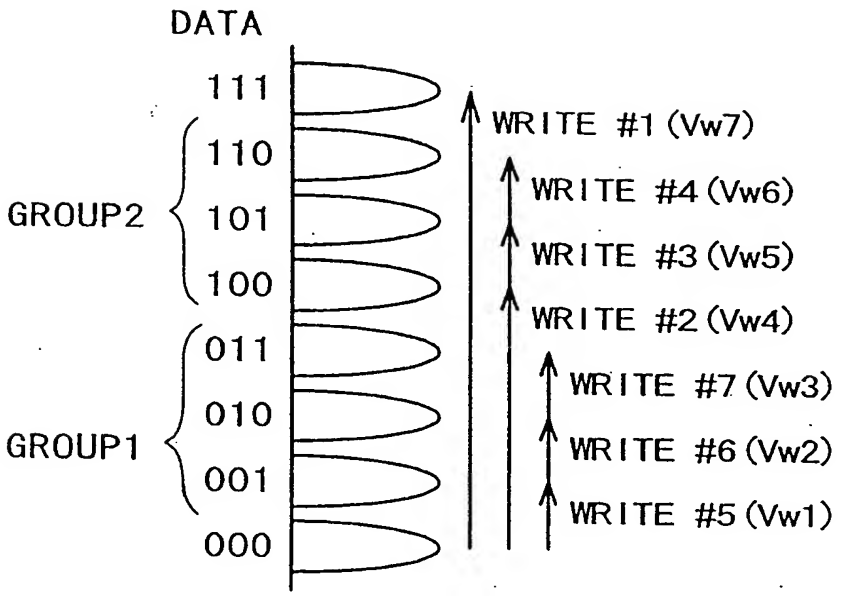

DATA
111
110
101
100
011
010
001
000
GROUP2
GROUP1
WRITE #1 (Vw7)
WRITE #4 (Vw6)
WRITE #3 (Vw5)
WRITE #2 (Vw4)
WRITE #7 (Vw3)
WRITE #6 (Vw2)
WRITE #5 (Vw1)

FIG. 10

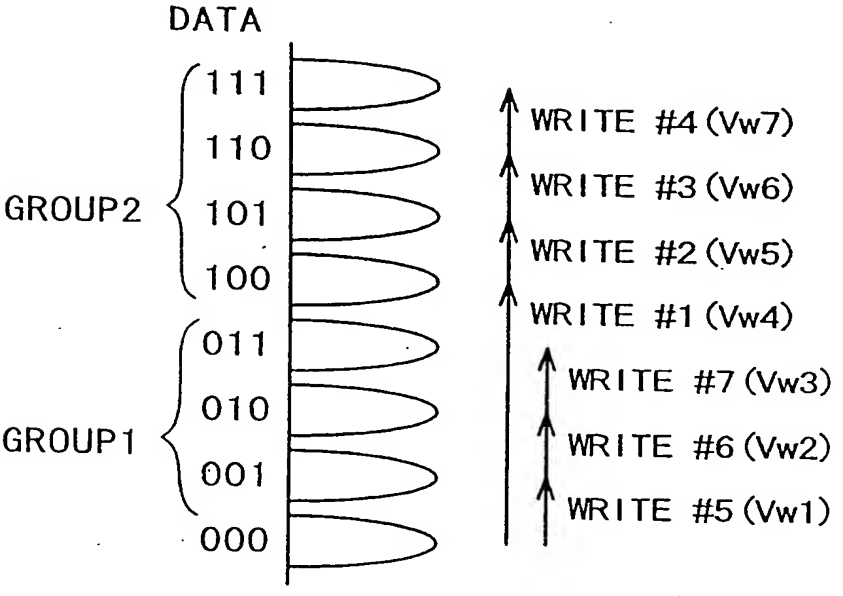

DATA
111
110
101
100
011
010
001
000
GROUP2
GROUP1
WRITE #4 (Vw7)
WRITE #3 (Vw6)
WRITE #2 (Vw5)
WRITE #1 (Vw4)
WRITE #7 (Vw3)
WRITE #6 (Vw2)
WRITE #5 (Vw1)

FIG. 11A

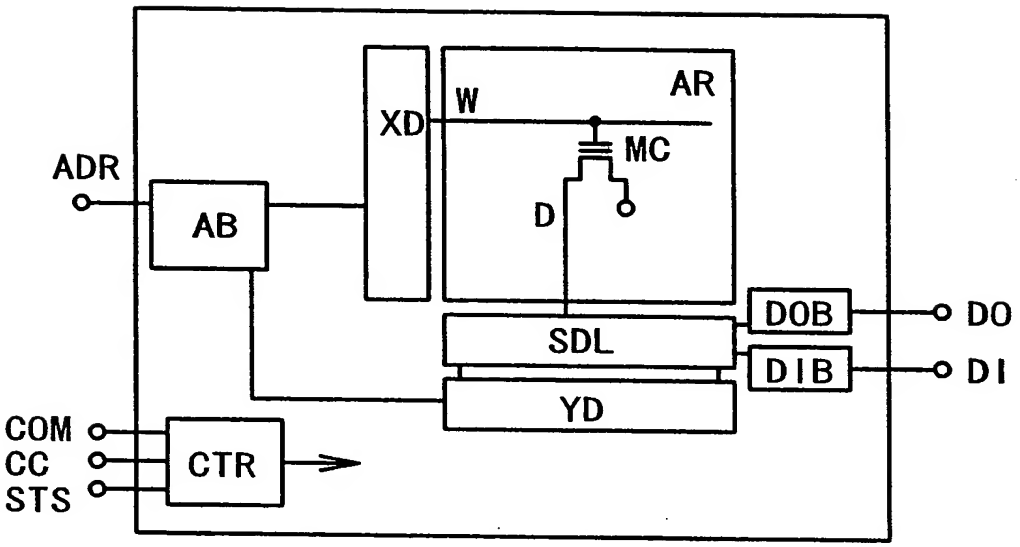


FIG. 11B

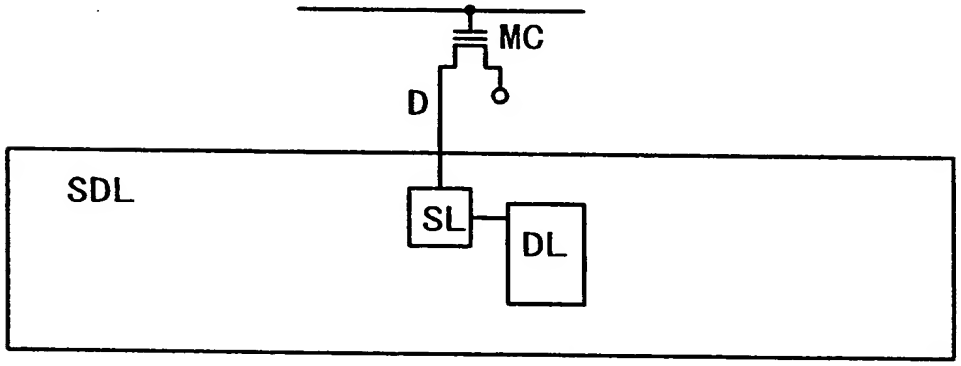
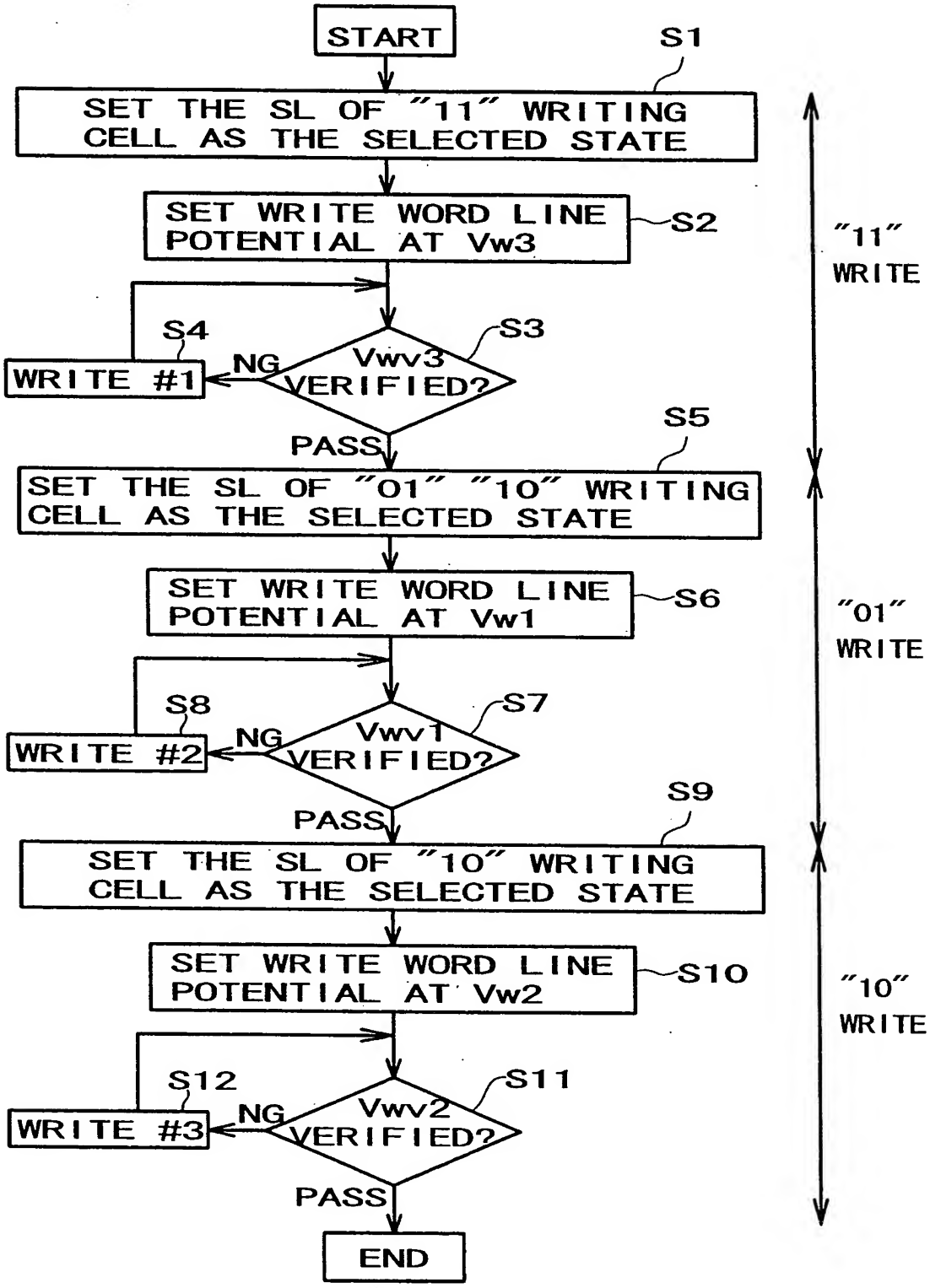


FIG. 12


START
S1
SET THE SL OF "11" WRITING CELL AS THE SELECTED STATE
SET WRITE WORD LINE POTENTIAL AT Vw3
S2
"11" WRITE
S4
S3
WRITE #1
NG
Vwv3 VERIFIED?
PASS
S5
SET THE SL OF "01" "10" WRITING CELL AS THE SELECTED STATE
SET WRITE WORD LINE POTENTIAL AT Vw1
S6
"01" WRITE
S8
S7
WRITE #2
NG
Vwv1 VERIFIED?
PASS
S9
SET THE SL OF "10" WRITING CELL AS THE SELECTED STATE
SET WRITE WORD LINE POTENTIAL AT Vw2
S10
"10" WRITE
S12
S11
WRITE #3
NG
Vwv2 VERIFIED?
PASS
END